

PATENT

DOCKET NO.: Intel 2207/1266902
ASSIGNEE: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Terry STERRETT et al.

DIVISIONAL OF :
SERIAL NO. : 09/986,409

PARENT APPLICATION
FILED : November 8, 2001

FOR : A METHOD AND APPARATUS FOR IMPROVING
AN INTEGRATED CIRCUIT DEVICE

GROUP ART UNIT : 2829

EXAMINER : Scott B. GEYER

M/S: PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate of Mailing	
I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail No. EV351180962US, in an envelope addressed to: M/S: PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Date: July 1, 2003	Signature: 
Barbara Vance	

PRELIMINARY AMENDMENT

Dear Sir:

In response to the Office Action dated May 30, 2002, please cancel claims 1-2 and 5-17 in this Divisional application leaving claims 3-4 and 18-27, prior to calculating fees.

Applicant elected to prosecute claims 1-2 and 5-17 (Group I) in the parent application and filed this Divisional application to prosecute the remaining claims, 3-4 and 18-27 (Group II). This application claims benefit of the November 8, 2001 filing date of the parent application.

Amendments to the Specification begins on page 2 of this paper.

Amendments to the Claims begins on page 4 of this paper.